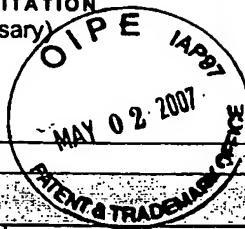


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2.	/C.N./	European Examination Report dated February 28, 2007, for European Application No. 04252390.2-1243.
3.		<del>Wang, P. A. et al: "New VLSI Architectures Of RSA Public-Key Cryptosystem", June 9, 1997 Vol. 3, pages 2040-2043 (duplicated)</del>
4.		<del>Behrooz, Parhami: "High-Radix Multipliers" Computer Arithmetic: Algorithms And Hardware Design, 2000, page 159, paragraph 10.2-161, figure 10.6</del>
5.		<del>Tenca, A. F. et al: "High-radix Design of A Scalable Modular Multiplier" cryptographic Hardware and Embedded Systems, 3<sup>rd</sup> International Workshop, May 14, 2001 Vol. 2162, pages 185-201</del>

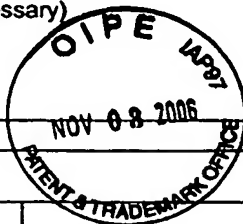
JAC/LFG/cm

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